Notice of Allowability	Application No. Applicant(s)		
	10/053,870	TANI ET AL.	
	Examiner	Art Unit	
	Steven J. Mottola	2817	
	Oteven 3. Mottola	2017	
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT I of the Office or upon petition by the applicant. See 37 CFR 1.31	S (OR REMAINS) CLOSED 5) or other appropriate come RIGHTS. This application is	in this application. If not includ munication will be mailed in due	led course. THIS
1. This communication is responsive to <u>September 23 2003</u>	! .		
2. The allowed claim(s) is/are <u>1-13 and 15-18</u> .			
3. The drawings filed on 24 January 2002 are accepted by the Examiner.			
4. ☐ Acknowledgment is made of a claim for foreign priority ura) ☐ All b) ☐ Some* c) ☐ None of the:	nder 35 U.S.C. § 119(a)-(d)	or (f).	
1. Certified copies of the priority documents have	e been received.		
2. Certified copies of the priority documents have	e been received in Applica	tion No	
3. Copies of the certified copies of the priority d	ocuments have been receiv	ed in this national stage applica	ation from the
International Bureau (PCT Rule 17.2(a)).			
* Certified copies not received:			
5. Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).			
(a) The translation of the foreign language provisional application has been received.			
6. Acknowledgment is made of a claim for domestic priority	under 35 U.S.C. §§ 120 and	d/or 121.	
Applicant has THREE MONTHS FROM THE "MAILING DATE" of below. Failure to timely comply will result in ABANDONMENT o	f this application. THIS TH	IREE-MONTH PERIOD IS NOT	EXTENDABLE.
7. A SUBSTITUTE OATH OR DECLARATION must be sub INFORMAL PATENT APPLICATION (PTO-152) which gives real			NOTICE OF
 8. CORRECTED DRAWINGS must be submitted. (a) including changes required by the Notice of Draftsper 1) hereto or 2) to Paper No 	rson's Patent Drawing Rev	iew (PTO-948) attached	
(b) ☐ including changes required by the proposed drawing	correction filed, wl	nich has been approved by the l	Examiner.
(c) including changes required by the attached Examine	r's Amendment / Comment	or in the Office action of Paper	No
Identifying indicia such as the application number (see 37 CFR each sheet.	1.84(c)) should be written on	the drawings in the front (not the	∍ back) of
9. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT FOR			Note the
Attachment(s)			
 1 ☐ Notice of References Cited (PTO-892) 3 ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 5 ☐ Information Disclosure Statements (PTO-1449), Paper No. 9 7 ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	4 ⊠ Intervi 0619 03 6⊠ Exami	of Informal Patent Application (ew Summary (PTO-413), Paper ner's Amendment/Comment ner's Statement of Reasons for	No. 100903
		Sieven J. Monola Primary Examina	

Application/Control Number: 10/053,870 Page 2

Art Unit: 2817

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or

additions be unacceptable to applicant, an amendment may be filed as provided by 37

CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than

the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr.

Brooks on October 9, 2003.

2. The application has been amended as follows: claim 18 has been rewritten in independent

form to include all of the limitations of canceled claim 14 and now reads as follows:

-- An operational amplifier, comprising:

a plurality of transistors including a transistor serving as a current source;

a bias generating circuit that applies a bias voltage to said transistor serving as a current

source; and

a bias voltage adjusting circuit that adjusts the bias voltage generated by said bias

generating circuit to change the current flowing through said transistor serving as a current

source,

wherein said bias voltage adjusting circuit comprises a plurality of switches for adjusting

the bias voltage among a plurality of values.--

jévén J. Mottola

fimary Examiner